

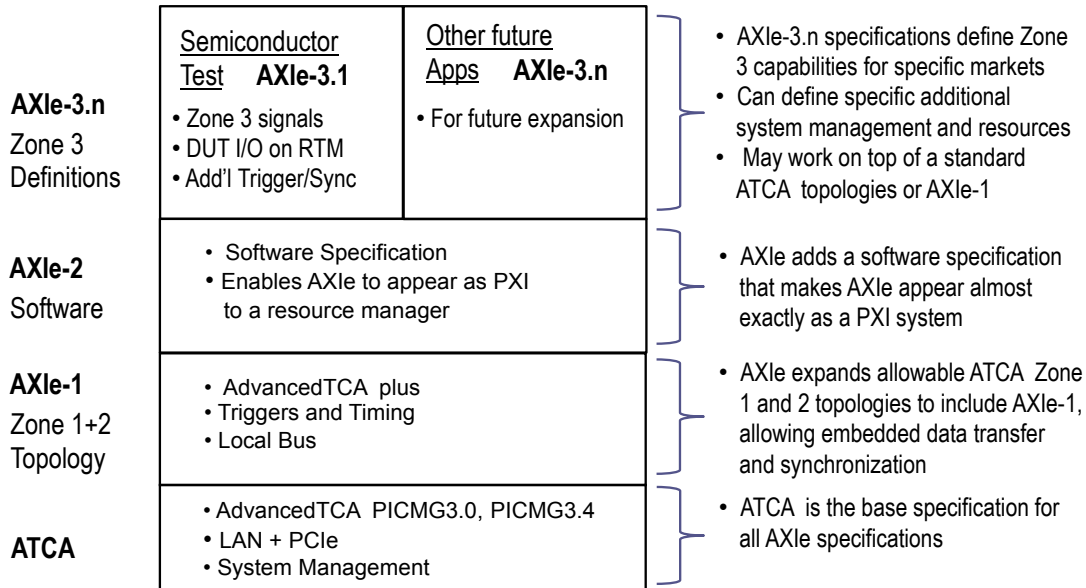
AXIe Releases Revision 2 of AXIe Base Architecture Specification

By [Larry Desjardin](#), [Modular Methods](#)

The AXIe Base Architecture Specification (AXIe-1) defines the standard for most AXIe instruments. It is layered on top of the AdvancedTCA industrial bus standard, with extensions for instrumentation. It is the second layer in the AXIe specification table below.

AXIe Specification Structure

AXIe is a scalable family of specifications allowing a portfolio of applications.



An additional specification is AXIe-2, a software specification to make AXIe products appear as PXI products to an instrument controller. Finally, AXIe-3.1 defines extensions for semiconductor test.

Revision 2 of AXIe-1 was released at the beginning of September after ratification by the AXIe Board of Directors. Revision 2 is the result of extensive interoperability testing between vendors, and modifies the standard to improve interoperability or define new capabilities. Examples of changes include e-keying for multi-slot modules, local bus electrical specifications, location of PCIe coupling capacitors, and correcting various typos in Revision 1.

The new specification may be downloaded [here](#).